

FIG. 1

PRIOR ART

108

29/10

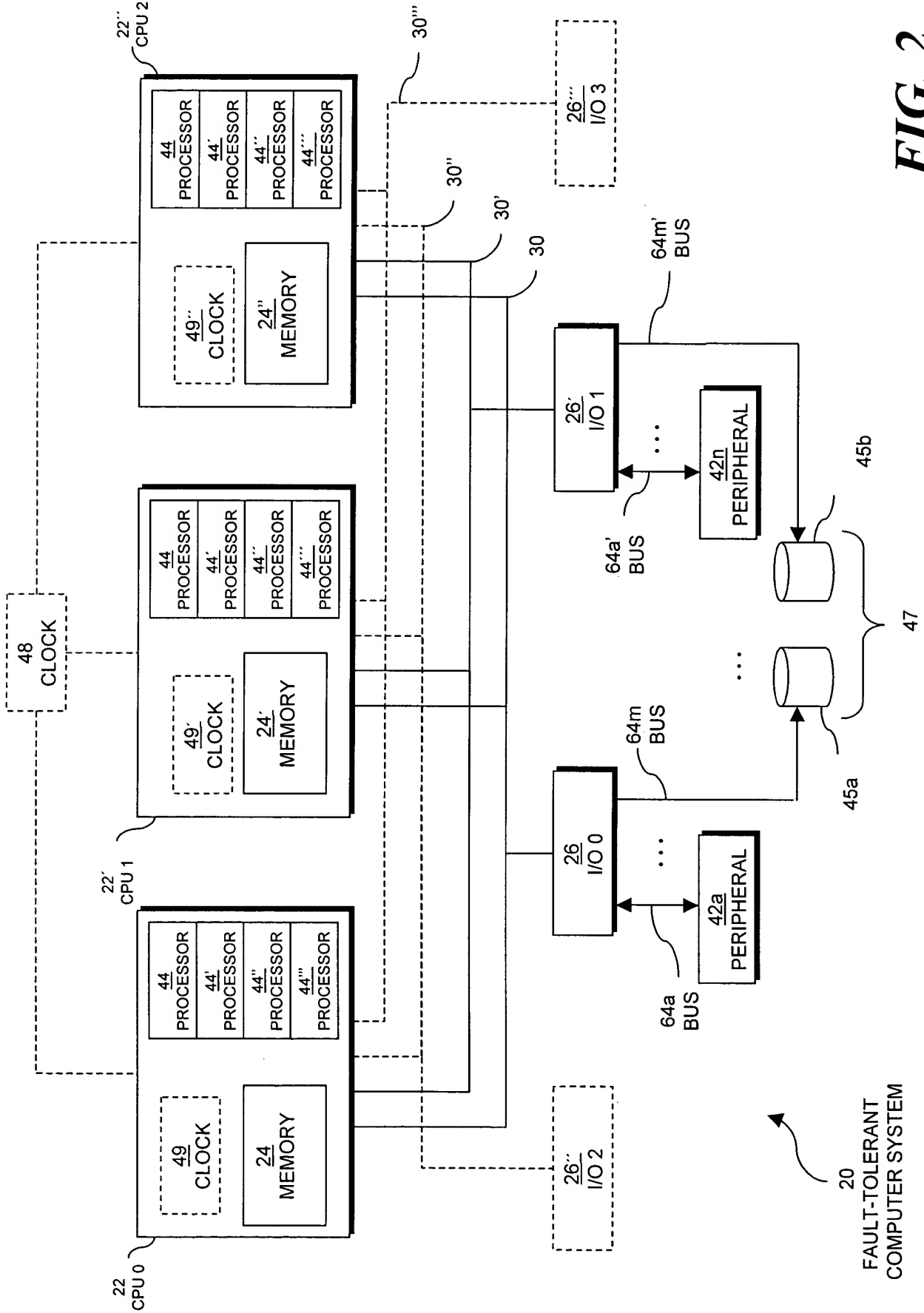


FIG. 2

FIG. 2 is a block diagram of a fault-tolerant computer system (20) according to one embodiment of the present invention.

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20  
 FAULT-TOLERANT  
 COMPUTER SYSTEM

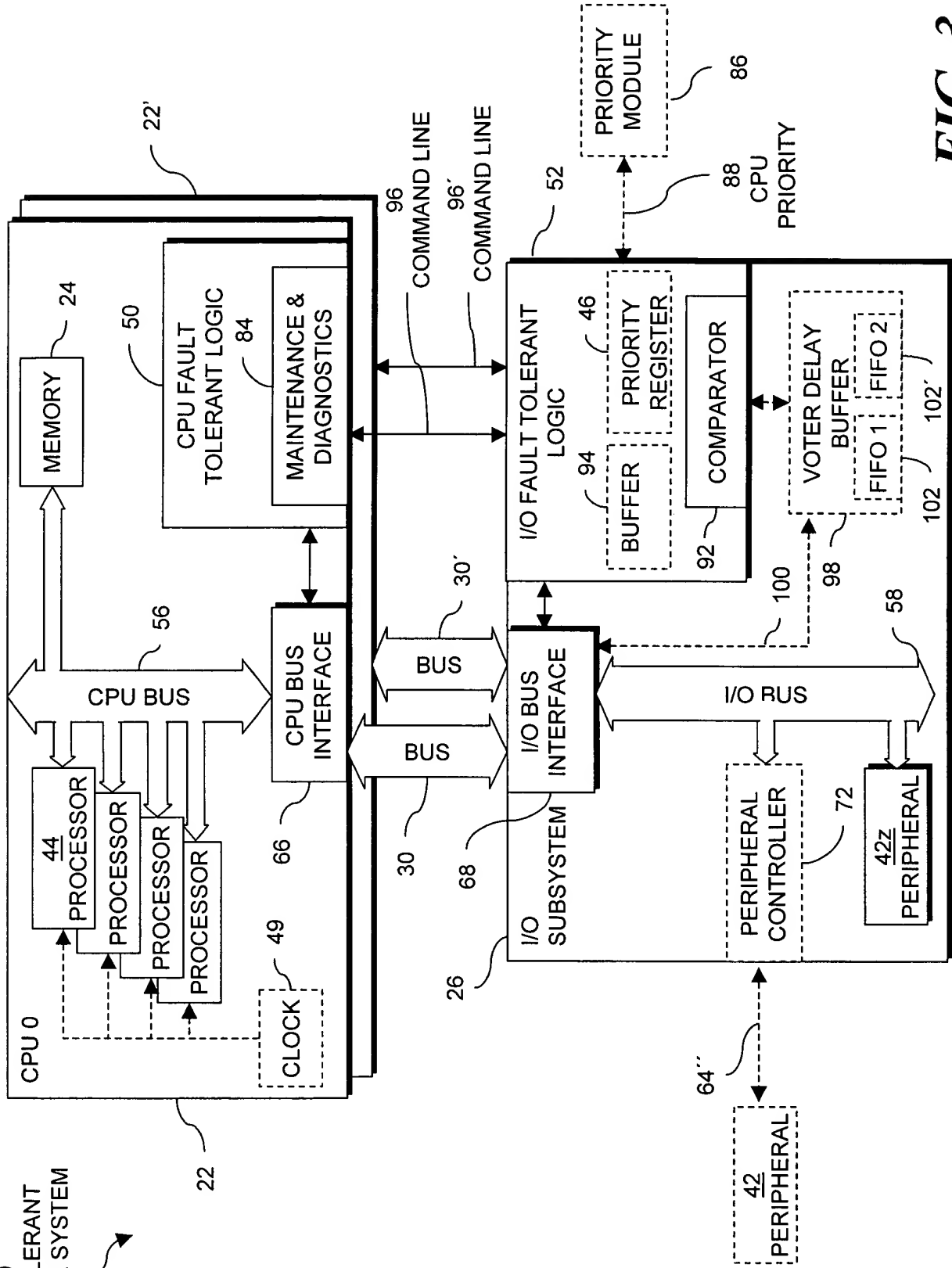
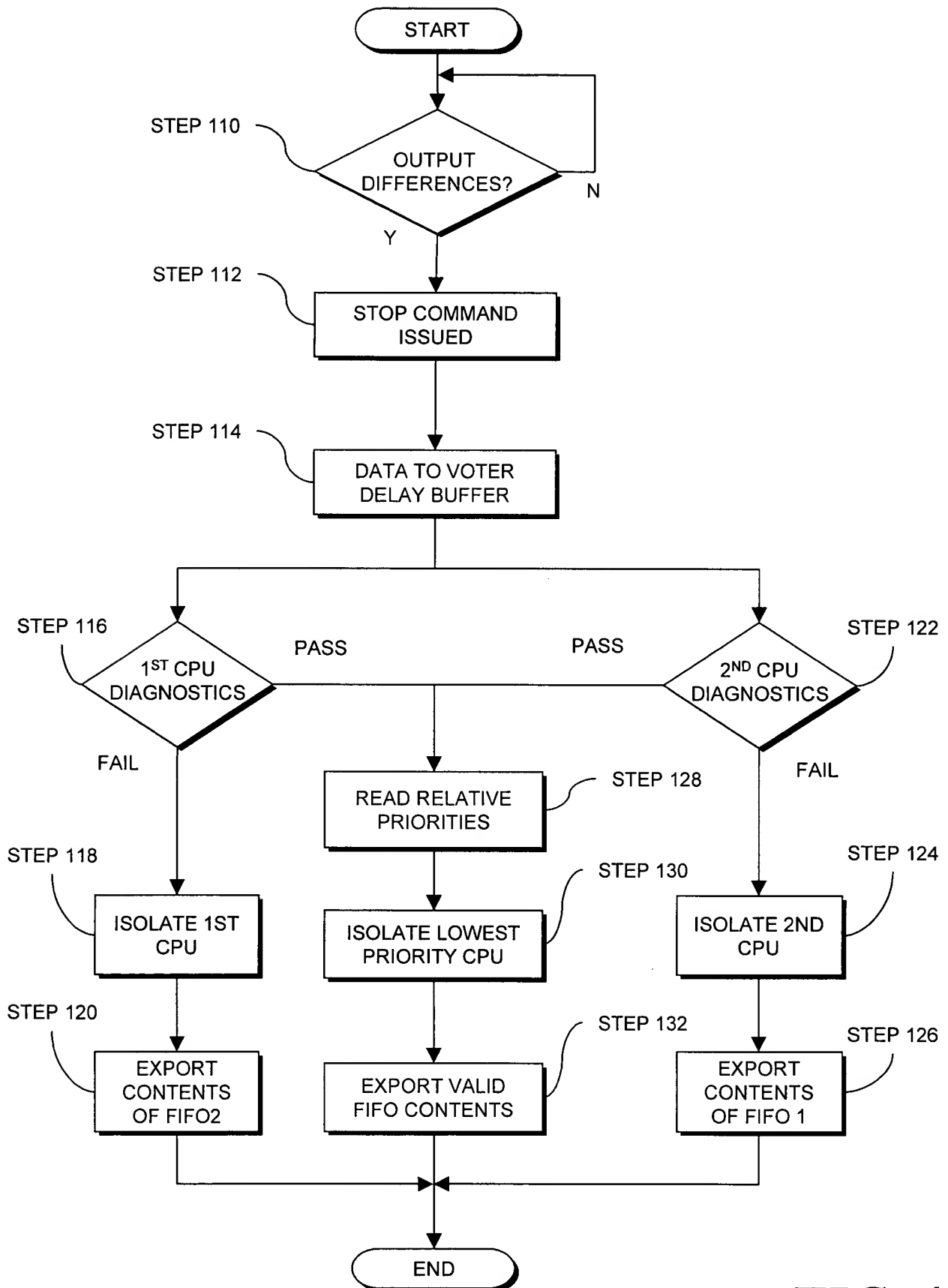


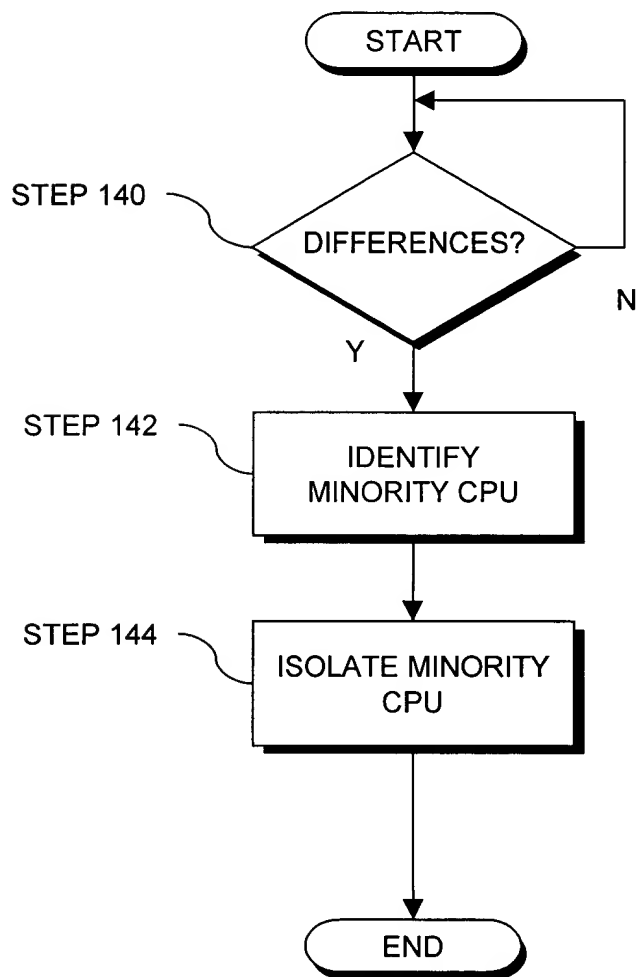
FIG. 3

4810



**FIG. 4**

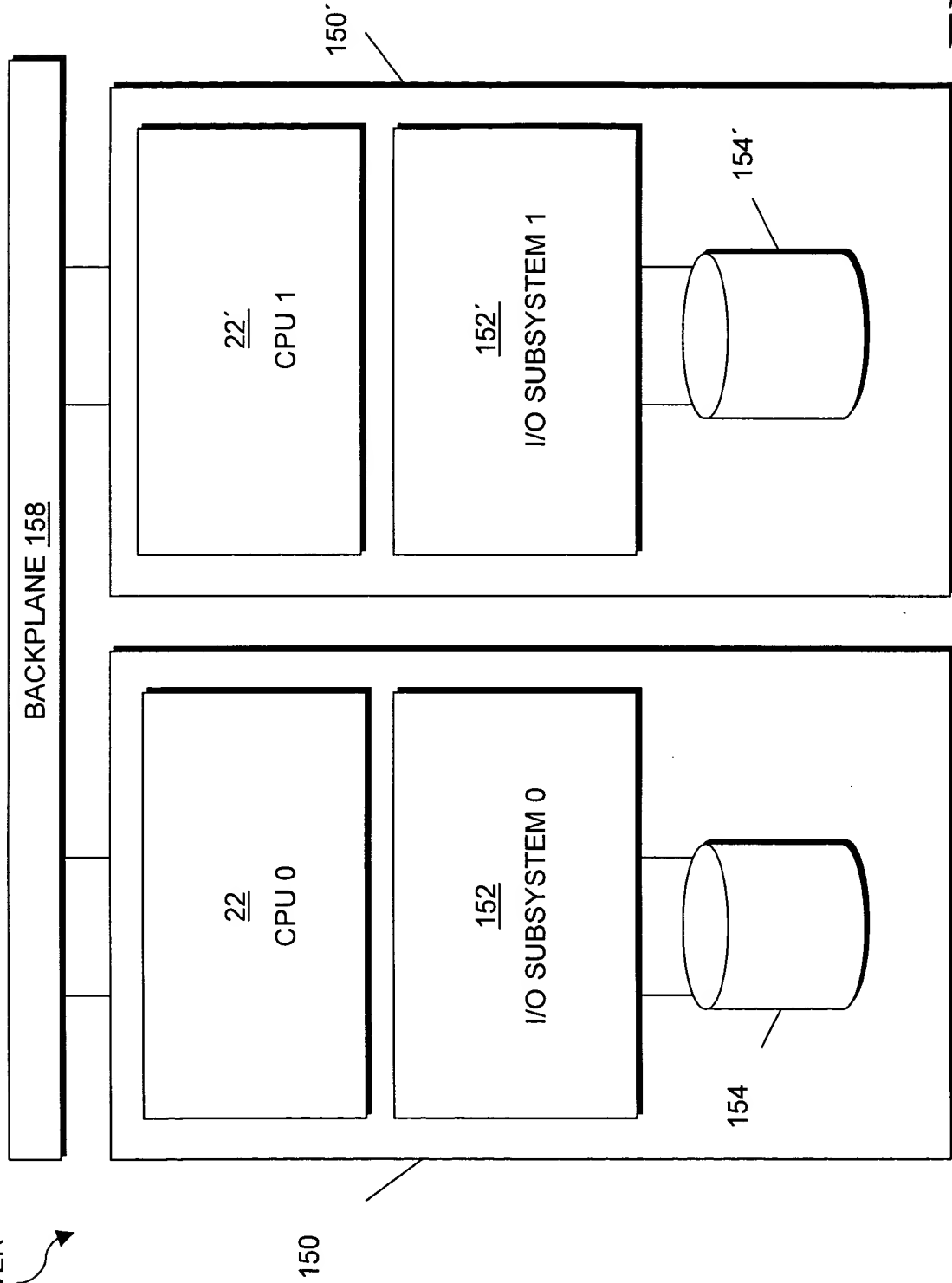
5810



**FIG. 5**

6816

20  
FAULT-TOLERANT  
SERVER



**FIG. 6**

7810

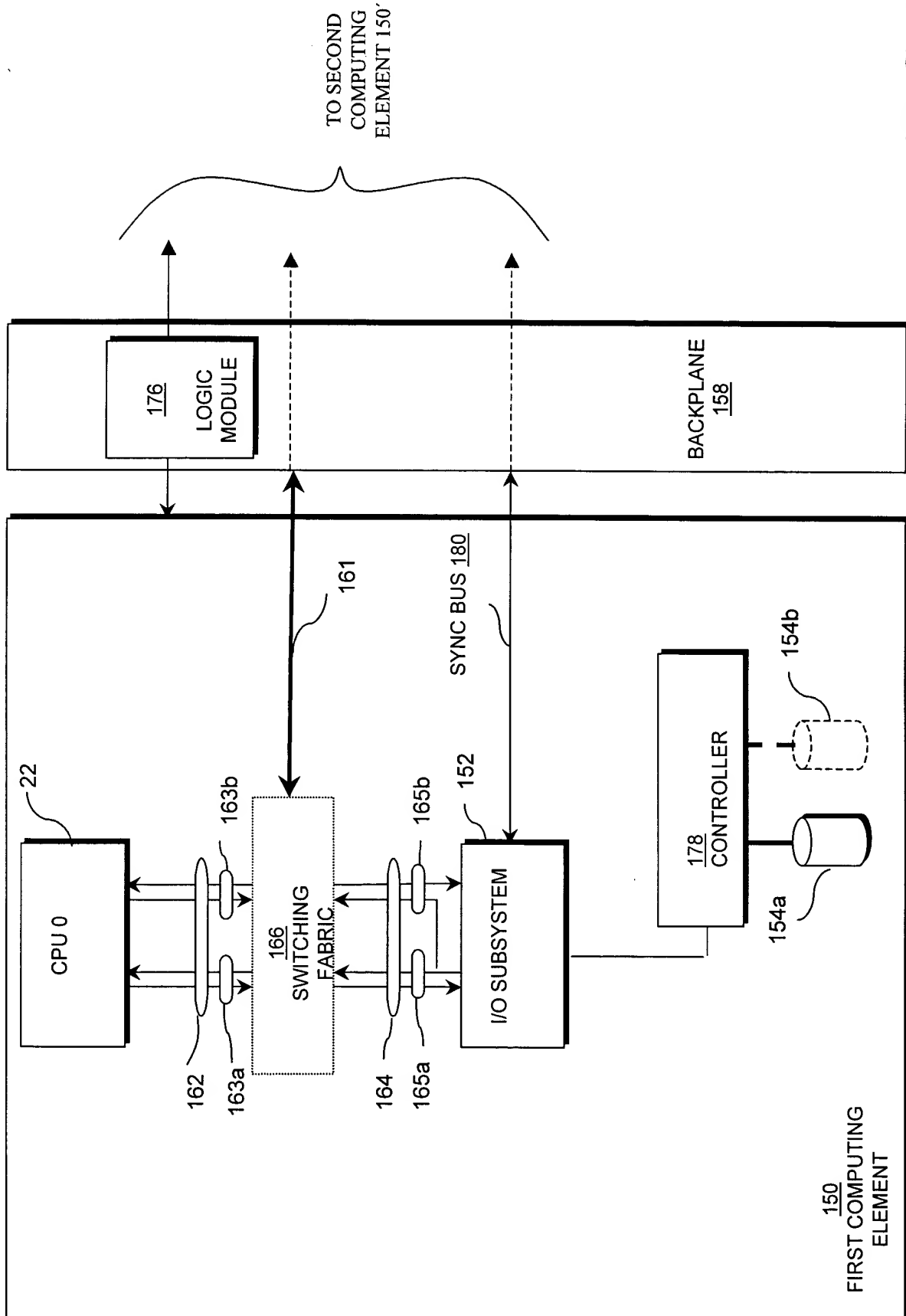


FIG. 7

8910

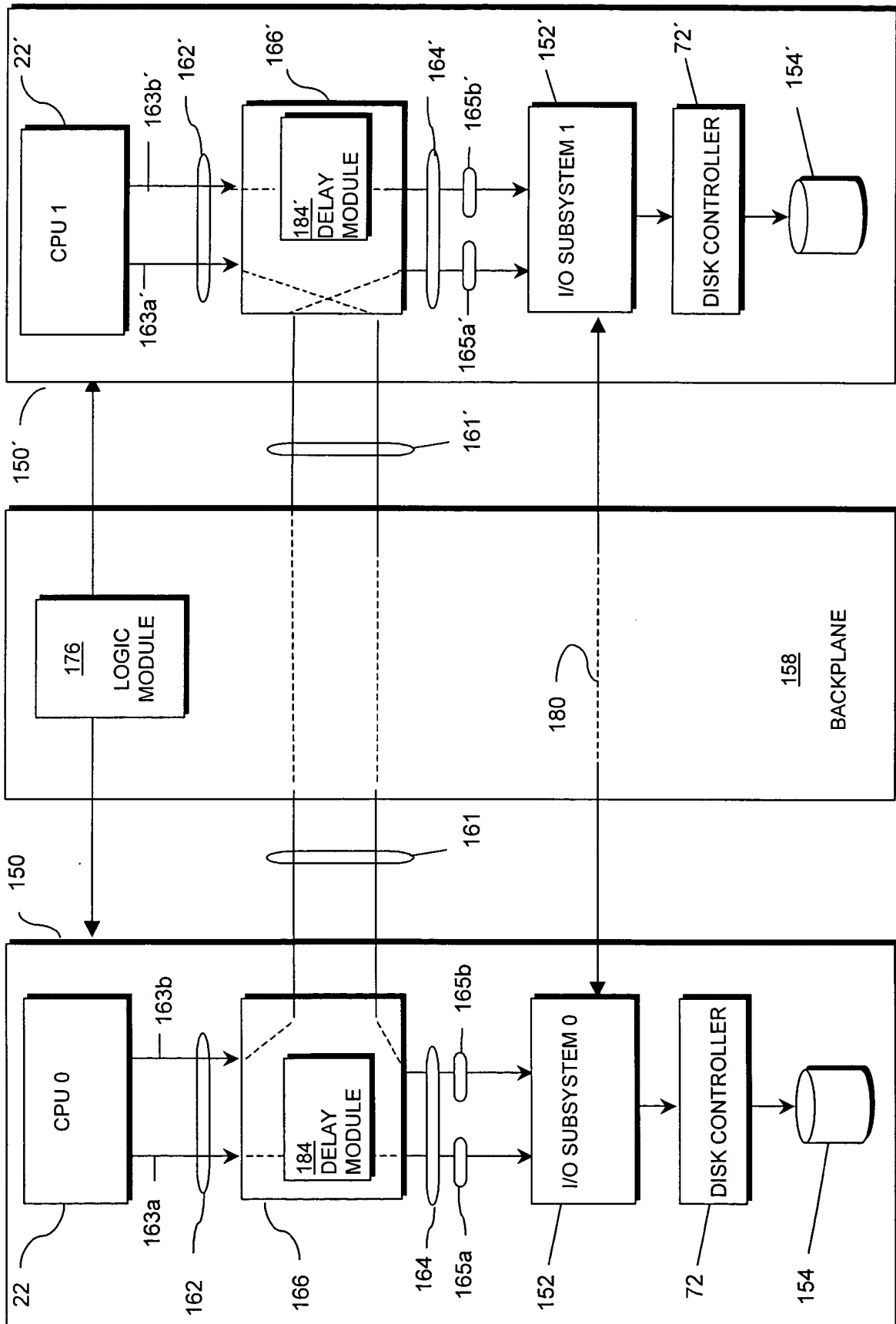
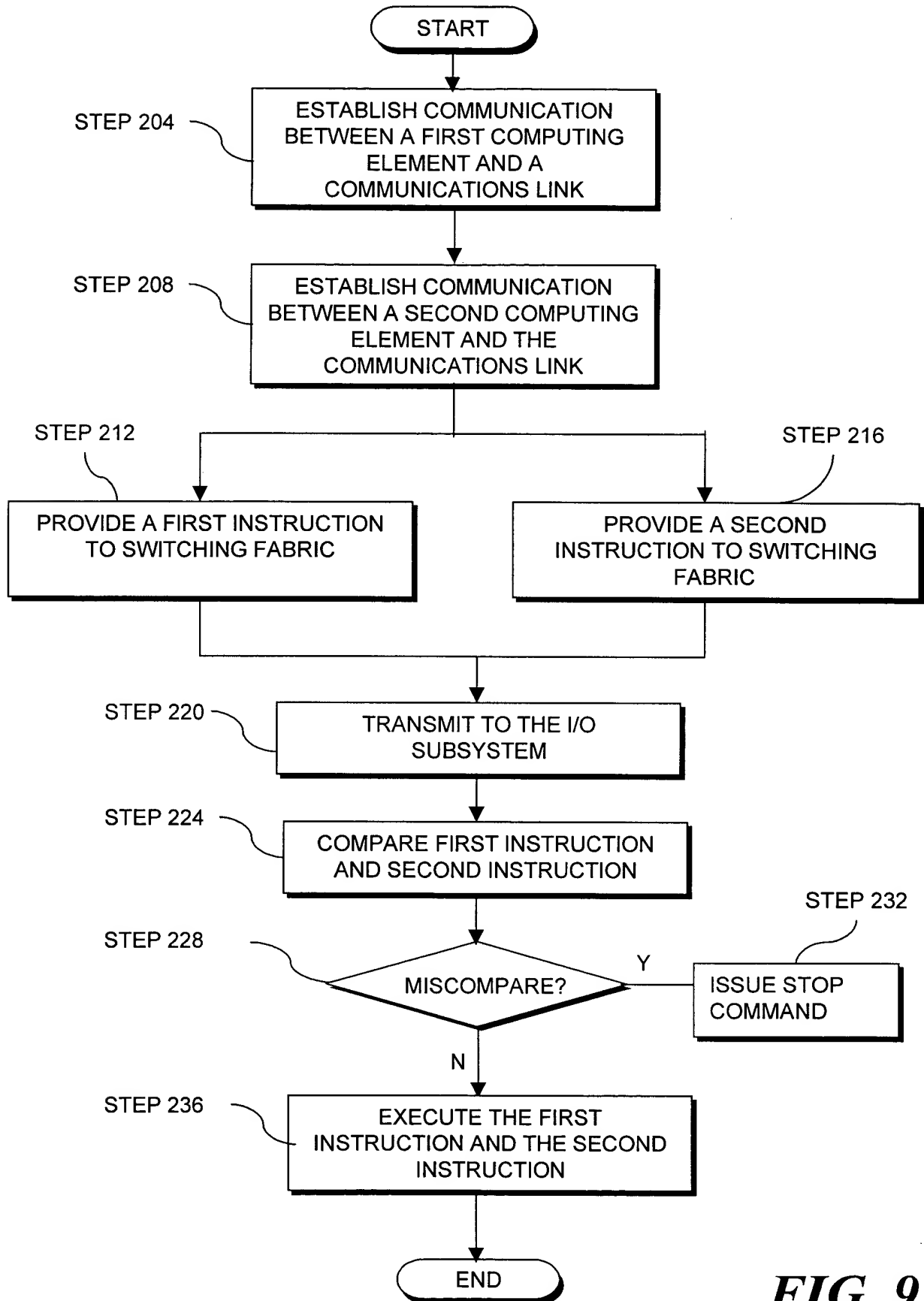


FIG. 8



9816



**FIG. 9**

1081

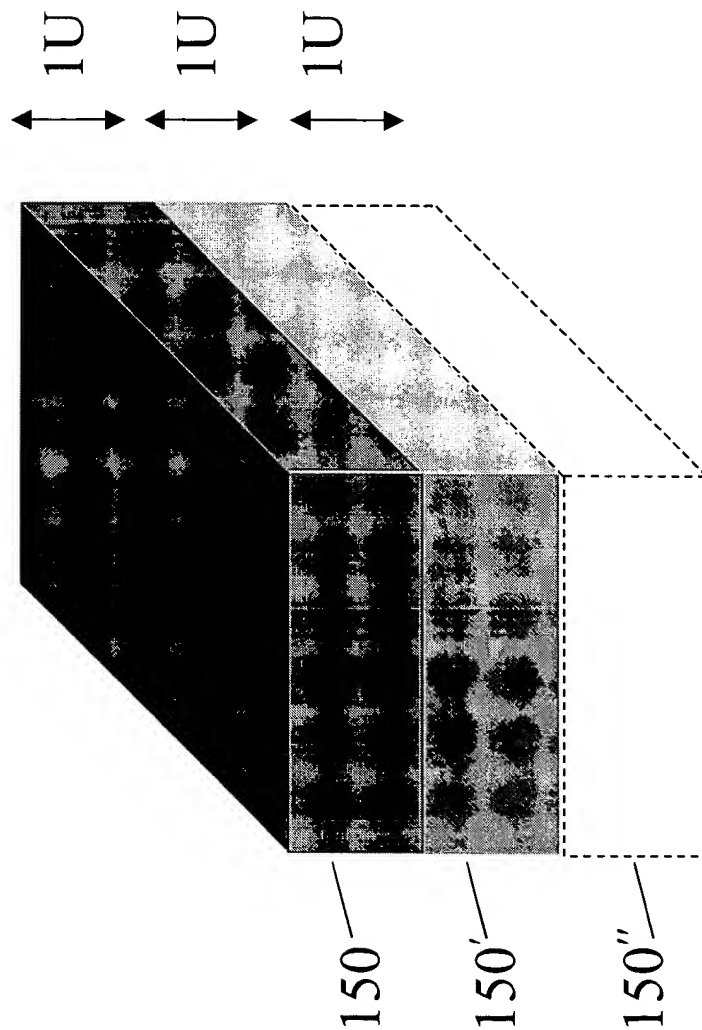


FIG. 10